

REMARKS

Claims remaining in the present patent application are numbered 1-23. The rejections and comments of the Examiner set forth in the Office Action dated March 19, 2003 have been carefully considered by the Applicants. Applicants respectfully request the Examiner to consider and allow the remaining claims.

35 U.S.C. §103 Rejection

The present Office Action rejected Claim 1 under 35 U.S.C. 103(a) as being unpatentable over Matsuzaki et al. (U.S. Patent No. 6,140,992), in view of Kim et al. (U.S. Patent No. 5,355,443), Hannah (U.S. Patent No. 5,038,297), and Yuki et al. (U.S. Patent No. 5,805,149). Also, Claims 2-4, 7, and 8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Matsuzaki et al. in view of Kim, Hannah, Yuki et al., and further in view of Ogawa et al. (U.S. Patent No. 6,018,331 and Singla et al. (U.S. Patent No. 6,597,373). Further, Claims 5, 6, and 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Matsuzaki et al. in view of Kim et al., Hannah, and Yuki et al. Moreover, Claims 10-17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Matsuzaki et al. in view of Kim et al., Hannah, further in view of Ogawa et al., and Singla et al. Also, Claims 18-23 are rejected under 35 U.S.C. 103(a) as being unpatentable over Matsuzaki et al. in view of Kim et al., Hannah, Yuki,

further in view of Ogawa, Singla, and He et al. (U.S. Patent No. 6323,849).

Applicants have reviewed the above cited references and respectfully submit that the present invention, as recited in Claims 1-23, is neither anticipated nor rendered obvious by the Matsuzaki et al. reference taken alone or in combination with the Kim et al., Hannah, Yuki et al., Ogawa et al., Singla et al. and He et al. references.

Independent Claims 1, 10, and 18

Applicants respectfully point out that embodiments of the present invention as claimed in amended independent Claims 1, 10, and 18 each recite, in part:

A display unit comprising:
a display panel comprising a pixel matrix comprising: an (m x n) pixel frame buffer region; and an x pixel border region for only displaying a display attribute, wherein said border region surrounds said frame buffer region . . .
a border attribute register for containing said display attribute for said border region, wherein said display attribute is selected to provide viewing contrast with image data located near said border region . . . (Emphasis Added)

Specifically, the present invention pertains to a controllable pixel border that surrounds a frame buffer region for improved viewability of a display device. That is, the pixel border displays a display attribute. For instance, the pixel border is useful for increasing

viewability, e.g., contrast, of images and/or characters that are displayed along the edge of a frame buffer region.

In particular, embodiments of the present invention as claimed in independent Claims 1, 10, and 18, have been amended to explicitly state, unlike the prior art references which do not disclose a controllable pixel border region, a pixel border region that displays a display attribute that is selected to provide viewing contrast with images and/or characters in the pixel frame buffer region that are located near the pixel border region (See Specification, page 20 lines 1-2).

Applicants respectfully note that the prior art reference, Matsuzaki et al., does not comprise nor suggest a controllable pixel border region that provides viewing contrast. In contrast to independent Claims 1, 10, and 18 of embodiments of the present invention, the Matsuzaki et al. reference discloses a display control system for controlling the display format to be displayed by a display apparatus. As such, the Matsuzaki et al. reference discloses a border section that displays border pixel data to frame a display image frame. However, Applicants respectfully point out the Matsuzaki et al. reference does not comprise or suggest a pixel border region displaying a display attribute that is selected to provide viewing contrast with images and/or characters near the border region, as in embodiments of the

present invention as claimed in independent Claims 1, 10, and 18.

Moreover, the Kim et al., Hannah, Yuki et al., Ogawa et al., Singla et al. and He et al. prior art references also do not comprise, suggest, or disclose a pixel border region displaying a display attribute that is selected to provide viewing contrast with images and/or characters near the border region, as in embodiments of the present invention as claimed in independent Claims 1, 10, and 18.

Thus, Applicants respectfully content that embodiments of the present invention as claimed in independent Claims 1, 10, and 18 are neither anticipated nor rendered obvious by the Matsuzaki et al. reference taken alone or in combination with the Kim et al., Hannah, Yuki et al., Ogawa et al., Singla et al. and He et al. references, and are in a condition for allowance. As a result, Applicants respectfully submit that Claims 2-9 which depend from independent Claim 1, as currently amended, are also in a condition for allowance as being dependent on an allowable base claim. Also, Applicants respectfully submit that Claims 11-17 which depend from independent Claim 10, as currently amended, are also in a condition for allowance as being dependent on an allowable base claim. Further, Applicants respectfully submit that Claims 19-23 which depend from independent Claim 18, as currently amended, are also in a

condition for allowance as being dependent on an allowable base claim.

CONCLUSION

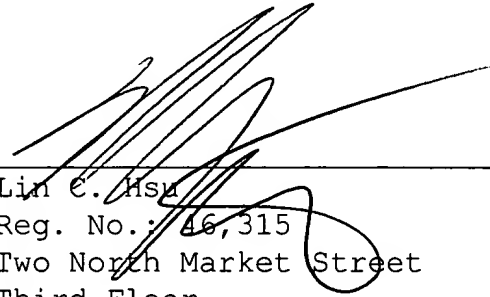
In light of the facts and arguments presented herein, Applicants respectfully request reconsideration of the rejected Claims.

Based on the arguments presented above, Applicants respectfully assert that Claims 1-23 overcome the rejections of record. Therefore, Applicants respectfully solicit allowance of these Claims.

The Examiner is invited to contact Applicants' undersigned representative if the Examiner believes such action would expedite resolution of the present Application.

Respectfully submitted,
Wagner, Murabito & Hao LLP

Date: 26 November 2003



Lin C. Hsu
Reg. No.: 46,315
Two North Market Street
Third Floor
San Jose, California 95113